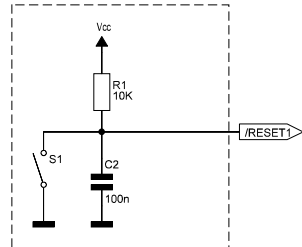
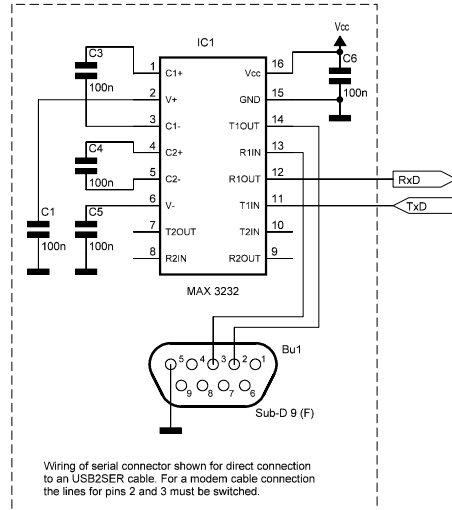


Z180 experimental setup, development stage 1



Serial Host Interface



Atmel control processor

